

PERSONAL INFORMATION Fabio De Angelis

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Gender Male | Date of birth 20 March 1976 | Nationality Italian

WORK EXPERIENCE

August 2019 – Present EGSolutions SpA

www.egsolutions.com

www.soiort.com

2020 Product Developement, Design of a mezzanine board for the Beagleboard X15.

- The boards is based on Atmega128 and implements a three axis stepper motor controller, USB interface, temperature sensors, power management for an ATX power supply, 5 volt buck converter for external dmd projector.
- The board interface with Texas Instruments Beagleboard-x15. Linux device tree was adapted for USART and pin control.
- 2019 Product Lifecycle
 - Part selection for obsolete components and cost reduction.
 - Technical documentation for the DScan-5 scanner.
 - Risk assessment for the DScan-5 scanner and its accessories according to EN-61010-1.
 - Inline electrical test according to UL-61010-1.

2015 – 2019 Sordina IORT Technologies SpA

2015 - 2019 R&D Engineer

- Project Manager for the development of the new version of LIAC control unit for compliance with third edition of IEC 60601.
- Software development from requirements to validation.

2013-2014 R&D Engineer

- Employed in the R&D department as electronic designer for the design and developing of the electron accelerator for IORT, LIAC-S.
- Development of the HMI with focus to usability and safety.
- Design, at system level, the hardware of the control unit, motion system, cooling system and tuning system.
- Development of the control firmware for the microprocessor modules of the LIAC-S device.
- FDA 510K submission for LIAC-S
- Commissioning of first LIAC-S in Saudi Arabia
- Execution of EMC test with CFDA inspector in NEMKO Labs for LIAC device
- 2009 System design, development and testing of an industrial electron accelerator.
 - System design of a fixed electron accelerator developed in collaboration with physics department of Messina. The system was designed to allow hospital waste sterilisation.
 - Installation and commissioning of the sterilisation plant.

2008 - 2012 Regulatory, manuals and Training

- Manuals, technical notes and documentation writing for FDA 510K submission.
 - Trainer for third party service engineers.

2006-2008 Circuit design, development and testing

- Microcontrollers programming and computer-based software;
- interfacing with technics and mechanic engineers for system development;
- Development of control electronic and firmware for a double conversion in line UPS.
 - Development of a SCR driver for cooling fans control.
 - Development of a power driver for a variable bending magnet. Firmware and Software was developed for remote control.
- Firmware development for LIAC control (Microchip Assembly and C18).
- 2006 Service and Installation Manager
 - Responsible for LIAC site preparation and installation.
 - Guard of field problems from service and installation engineers.

2005 Simitecno srl

System developer.

Working in the R&D department "theory and practice of knowledge" ENEA's research group. The aim of the project was to develop an hardware system, for environmental control, based on the new "emulative olocontrol" paradigm. During this project a frame grabber was designed and developed to grab image from a PAL camera. An hardware edge detecting module was also designed and developed.

2005 TELSI srl

System developer, prototyping and testing.

During this period I learned laboratory instruments like oscilloscope, function generator, Spectrum analyzer and Network analyzer. I participated to development of a transmission line and varicap tunable filter, performing simulation with Microwave Office, development of a prototype PCB and testing of the prototype.

www.simitecno.it

www.telsisrl.it

September 20, 2020

EDUCATION AND TRAINING

7/2020 Agile and Scrum for software developement

Software development with Agile methodology with Scrum management framework. Agile24 Italy https://www.agile42.com/it/

3/2013 Software Requirements for Medical Device EU and US

Medical Device Software Training programme. Donawa Lifescience Consulting Srl, Rome, Italy

10/1995 – 10/2004 Electronic Engineering Degree

Electronic Engineering, microelectronic, final mark 100/100 cum laude, The Thesis, "Chaotic Circuit analysis for audio watermarking", presents a chaotic signal based watermarking system applied to audio signal fingerprinting. Some state of the art circuit topologies were analyzed for generation and coherent recovery of chaotic signals. An innovative steganography algorithm was developed for embedding a chaotic watermark in audio signals.

University of Rome "Tor Vergata".

7/2003 13th ITSS

International Travelling Summer School in optoelectronica and microwave. Technische Universität Darmstadt, Frankfurt, Germany.

9/1991 – 7/1995 Scientific High School Diploma

"Liceo Scientifico Cavanis" (Rome)

PERSONAL SKILLS

Mother tongue Italian

Other languages	UNDERSTANDING		SPEAKING		WRITING
	Listening	Reading	Spoken interaction	Spoken production	
English	C1	C2	C1	C1	C1
Spanish	A1	A1	A1	A1	A1

Electronic CAD - Orcad, Kicad;

- Pspice, Microwawe Office;
- MapleV, Matlab.

Microcontrollers - Pic18 family, C18 and Assembly in MPLab IDE;

- Atmel, C, AtmelStudio IDE;
- STM32 family, Eclipse.
- Computer & skills C++ (QT), Assembly (PIC micro); - Python, tcl/tk; - PHP, HTML, CSS, Javascript;
 - Linux (basic level);

Driving licence A